Keep Photonics Under Control: How to Harness Programmable Photonic Circuits

Presented by:



The OSA Optoelectronics (PO) Technical Group Welcomes You!

KEEP PHOTONICS UNDER CONTROL: How to Harness Programmable Photonic Circuits

OS.

Optoelectronics Technical Group

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Technical Group Leadership 2020







Vice Chair Daniele Melati National Research Council Canada, Canada

Chair **Winnie Ye** Carleton University, Canada Webinar Organizing Volunteer Jens H. Schmid National Research Council Canada, Canada



Technical Group at a Glance

• Focus

- The OP TG focuses in the field of semiconductor lasers, amplifiers, LEDs and super luminescent diodes, and other areas related to optoelectronics
- Over 4,500 members within OSA

Mission

- To benefit <u>YOU</u>
- Webinars, e-Presence, publications, technical events, business events, outreach
- Interested in presenting your research? Have ideas for TG events? Contact <u>winnie.ye@carleton.ca</u>

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Today's Webinar

Keep Photonics Under Control: How to Harness Programmable Photonic Circuits



Dr. Andrea Melloni, OSA Fellow, is Full Professor at Dipartimento di Elettronica, Informazione e Bioingegneria at Politecnico di Milano where he leads the group of Photonic Devices. With a background in microwaves, his field of research is in the analysis, design, characterization and exploitation of passive integrated optical devices for telecom and sensing. He is one of the pioneers of the slow light concept and its exploitation in the linear and nonlinear domains. In September 2008 he founded the company Filarete with the aim of developing and commercializing ASPIC (www.aspicdesign.com), the first circuit simulator for integrated optical circuits. He is active in characterization and testing techniques (from wafer to module testing), numerical methods in photonics (stochastic), development of high index contrast dielectric materials (SiliconOxyCarbide), design and analisys of photonic integrated circuits, biosensing (with exploitation of magnetic beads). Recently, he mainly focused on the control and stabilization of photonic circuits with a new technique of non invasive light monitors based on the natural surface state absorption phenomena occurring in waveguides. These activities are at the basis of the dynamic management and control of large and complex photonic integrated circuits for reconfigurable, programmable, locking and adaptive functionalities.





MILANO 1863

Dipartimento di Elettronica, Informazione e Bioingegneria



Keep Photonics Under Control: How to Harness Programmable Photonic Circuits

Andrea Melloni Politecnico di Milano, Italy

Summary



- Motivations and needs
- The ingredients:
 - monitors, actuators, electronics
 - techniques and algorithms:
 - Thermal management
 - Modulated signal for tuning
 - Pilot tones
- The recipes
- The dishes:
 - Filter tuning and operation
 - Look up table generation
 - Mode unscrambling
 - Dispersive media compensation

An Add-Drop Bandpass filter...





4 °C 1 BW shift

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A complex device...



16 filters
64 rings
32 Mach-Zehnder
32 modulators
16 PD

....



PICs: uncertainties and variability



... fabrication tolerances





Courtesy of IBM, 2007 F. XIA, et al, Nat. Photonics, 2007 1nm tolerance in waveguide width, 100 GHz wavelength shift

... stochastic nature of parameters





... temperature dependence

Material	K _{th} [K ⁻¹]	$\Delta f/\Delta T [GHz K^{-1}]$		
SiO ₂	10-5	1.5		
Si, InP,	2 10-4	10		

... operational conditions



PICs: adaptivity and programming

... non linear effects





... programmable integrated photonics



Y. Shen et al, Nat Photonics 11 (2017)



D. Perez et al., Nat Communications 8:636 (2017)

... adaptive tuning and locking to "external" drifts





L. Zhuang et al., Optica 2 (2015)

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(Negative) Feedback for control ...





- Human body temperature
- Production of human red blood cells vs oxygen
- Population of predators and prey
- The photosynthesis in plants vs CO₂ level
- Being reprimanded for coming to work late
- Economic: supply and demand law
- Several examples in mechanical ...



... also photonics needs feedback and control !!

The photonic chip as a system: the control layer





MOTIVATIONS

Fabrication tolerances Uncertainties and variability Stochastic nature of parameters **Temperature dependence Operational conditions Nonlinearities** Programmable photonics Adaptive photonics

The control layer from literature (incomplete!)



Dithering, analog Columbia Univ. 2014



Bang-bang, digital 15 bits Oracle 2014



X. Zheng, Opt. Express, 22(10) 2014

Tuning (peak search, analog) + locking (bang-bang, digital)

HP 2016



K. Yu, et al., JSSC, 51(09) 2016



Single Tx/Rx channel macro



TeraPHY: A High-density Electronic-Photonic Chiplet, OFC 2019 - Avar Labs, Inc.

TeraPHY die

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The ingredients





The control layer





The control layer: monitor





Light monitors: Ge, InP, hybrid, monolithic... CLIPP !



On-chip photodetection is a mature technology but... power hungry and photon hungry Ge on Silicon III-V compounds







Silicon itself can be used for light detection in the near-IR



H. Chen et al., APL 95, 171111 (2009)



M.W. Geis et al., PTL 19(3), 2007



H. Jayatilleka et al., 6 (1) OPTICA, 2019

Surface and Defect state absorption PDs

Photogeneration due to natural and/or induced (ion implantation) defect states at the edges of the waveguide core (symmetry breaking & dangling bonds)

A transparent detector: the CLIPP concept

ContacLess Integrated Photonic Probe lockin detection of photoconductance



A transparent detector: the CLIPP concept



ContacLess Integrated Photonic Probe (CLIPP)





Contactless capacitive access to the waveguide

Measuring the SSA induced waveguide conductance change ΔG through a lock-in detection circuit



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nature

research highlights

photonics

CLIPP performance











Performance match monitoring requirements:

- **Compact size:** *L* down to 25 μm
- Sensitivity down to -40 dBm
- 40 dB dynamic range
- **Speed down to 20 μs**
- □ Both TE/TM polarizations
- Arbitrary waveguide geometry (single-mode/multimode)
- No loss, no backreflection, no amplitude/phase perturbation, no need for doping



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The control layer: Actuators







Phase / Amplitude

Analog / Digital

Reversible (tuning, switch) / Permanent (trimming, programming) Fast (MHz for tuning/stabilization) / Slow (reconfiguration)

Compact (1-100 μm)

Low Power (< mW) / Energy consumption (< pJ/bit)



Phase actuators

ON/OFF $\rightarrow \Delta n = \pi$ (large Δn , moderate Δk)

$$\Rightarrow \frac{\Delta n}{\Delta k} \ge 250$$

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Integrated optical actuators



Thermal actuators



Si channel waveguide with embedded Si heater (n-doped)



p-n junctions Carrier injection/depletion



W.M. Green et al., Opt. Express 15 (2007)

Plasmonic memristor



MEMS based switches



S. Han et al., Berkeley, (2015)

Graphene, MoTe₂, ITO modulators



R. Amin et al., arxiv (2018)

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Integrated optical actuators



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Silicon OxyCarbide with ultra high TOC



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The control layer: electronic





Electronics at service of photonics









CMOS 0.35µm

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-ocal Analog Contro

REFLEX AR

Feedback and control – Reflex Arc





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Feedback and control by dithering – Reflex Arc



- **Dithering tecnique to** extract **partial derivatives** (2 frequencies)
- Integral controller locking heaters to the desired working point
- Control loop bandwidth affects speed and accuracy of locking





Handling multiple degrees of freedom





The control layer: algorithms and techniques





Feedback control of thermal xtalk



8x8 Si photonic switch matrix







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Feedback control of thermal xtalk



8x8 Si photonic switch matrix





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Feedback control of thermal xtalk



8x8 Si photonic switch matrix



Handling thermal cross talk





A thermal-xtalk free system is achieved (orthogonal coordinates):

$$\underline{\Delta\Psi} = \begin{pmatrix} 1 & 0 & 0 & \cdots & 0 \\ 0 & 1 & 0 & \cdots & 0 \\ 0 & 0 & 1 & \cdots & 0 \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ 0 & 0 & 0 & \cdots & 1 \end{pmatrix} . \underline{\delta\Psi}$$

F. Morichetti et al., JLT 1/2019

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Phase coupling matrix **T**

Actual

changes

phase $\Delta \Phi =$

Desired

changes

 $.\delta\Phi$ phase

Use of the TED technique

Estimation of Cross talk



matrix T

- TED technique can be adopted to **cancel phase coupling** in *arbitrary* PICs and *arbitrary* algorithms for tuning, locking, optimizing, switching...
- Circuit modifications are achieved in direction of $\delta \psi_i$ to minimize the error function
- (At each iteration) desired phase changes in each phase shifters are calculated by $\delta \underline{\Phi} = P \delta \underline{\Psi}$ and applied via thermal actuators
- *T* is the phase coupling matrix

Canceling thermal cross-talk effects in photonic integrated circuits M. Milanizadeh et al., JLT 37 (4), 2019

- Optically measured (exact)
- Electrical measured (symmetrical)
- Estimated (simulations)



 $T = \begin{pmatrix} \delta & 1 & \varepsilon & \eta \\ \mu & \xi & 1 & \theta \end{pmatrix}$

TED demo on tuning of coupled MRR in SiON





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Signal assisted tuning





CW

λ



Signal assisted tuning









100 Gbit/s **QPSK Module**

CW

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20 um

Filter

Pilot tones for device control (WDM regime)





Pilot tones for device control (WDM regime)







Pilot tones for device control (WDM regime)









The control layer: assembly





A multiphysics world !!







RF connections, reflections and crosstalk

Thermal management

Stress and strain

....

Time varying phenomena

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Let's use the ingredients !







Reconfigurable hitless filters





Filter design nominal values:

- 1 THz (8 nm) of Free Spectral Range (FSR)
- 40 GHz of 3 dB bandwidth
- 20 dB in band isolation



CLIPP detector at the Drop port

Mach-Zehnder Modulators (MZM) in the add port to apply optical label

Reconfigurable hitless filters

- Exploit **TED** to actuate MRRs
- Light monitor at the Drop port (CLIPP)
- Marking the added channel with a label
- Added channel is modulated





Fine tuning



Automatic tuning of the transfer function





Reconfigurable hitless filters





Automatic tuning and locking algorithm:

- 1. Disconnect filter from the bus and add/drop
- 2. Coarse tune of rings with a Look Up Table
- 3. Connect filter to bus and add/drop
- 4. Fine tuning and automatic locking of the filter



Look Up Table automatic generation





Voltages of heaters for one channel of LUT

Wavelength [nm]	Ring Top [V]	Ring Middle [V]	Ring Bottom [V]	MZ Connected [V]	MZ Disconnected [V]
1559,25	3,071	2,986	2,752	3,245	1,436

Unscrambling light (with reflex arc control and pilot tones)











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Phase front reconstruction





Mesh configured to maximize power at detector



Beam size at the phase mask 1.4 mm



Perturbed Pattern



Phase front reconstruction



Mesh configured to maximize power at detector



Beam size at the phase mask 1.4 mm

Mesh realigns compensating for phase-front perturbation





Now Mesh is reconfigured compensating for phase mask effects...

Compensated Pattern







Take home messages



Transparent monitors, efficient actuators

Electronics: Brain vs Reflex Arc, digital vs analog

Microfluidio channel

Strategy: TED, Pilot tones, tuning assisted by signals

Every device has its own control procedure

source microring (100 μm length)

Sensing arm/window

length) spot-si

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http://www.bboi.eu



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